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| <h1>TRANSMITTAL FORM</h1> <p>(to be used for all correspondence after initial filing)</p> | Applicati n Number | 09/716,734 | |
| | Filing Date | Nov. 20, 2000 | |
| | First Named Inventor | Chan et al. | |
| | Group Art Unit | 2826 | |
| | Examiner Name | A. O. Williams | |
| Total Number of Pages in This Submission | 5 | Attorney Docket Number | 00100.01.0012 |

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| ENCLOSURES (check all that apply) | | |
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| Remarks <div style="border: 1px solid black; height: 40px;"></div> | | |

| SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT | |
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| Firm or Individual name | Christopher J. Reckamp Reg. No. 34,414 |
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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Chan et al.
Serial No. 09/716,734
Filing Date: Nov. 20, 2000
Confirmation No. 7999

Examiner: Alexander O. Williams
Art Group: 2826
Our file no. 00100.01.0012
Docket No. 0100.0100120

10-2800
REC'D
Clerkin
3 #/amended
3/22/02
R/Brown

Title: INTEGRATED CIRCUIT PACKAGE AND METHOD OF FABRICATING SAME

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2/28/02 Rosalie Swanson
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RESPONSE TO RESTRICTION REQUIREMENT

In the Claims:

Please amend Claim 21 as follows:

In particular, please substitute the below claim for the indicating pending claim with the same number:

SUB
B1 21. (Once Amended) A method for fabricating an integrated circuit, the method comprising the steps of:

a1 providing a first substrate, the first substrate including at least one heat-generating circuit and having a first coefficient of thermal expansion;

providing a second substrate, the second substrate having a second coefficient of thermal expansion that is substantially equal to the first coefficient of thermal expansion;

thermally coupling the first substrate to the second substrate, the second substrate thermally conducts heat generated by the at least one heat-generating circuit away from the at least one heat-generating circuit.